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PATENT

**IN THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application. Please amend the claims to reflect the following:

1. – 27. (Canceled)

28. (Original) A target simulator for testing a radar system having  $n$  antenna ports comprising:

a processor adapted to receive target and waveform parameters and in accordance therewith calculate time-domain digital data samples representing a composite radar return waveform;

a digital to analog converter adapted to convert said digital samples to an analog signal;

an upconverter adapted to convert said analog signal to a radio frequency signal; and

a radio frequency splitter adapted to split said radio frequency signal into  $n$  output signals.

29. (Original) The invention of Claim 28 wherein said target simulator further includes first means for applying a delay to each of said  $n$  output signals.

30. (Original) The invention of Claim 29 wherein said first means includes  $n$  radio frequency time delays, each delay adapted to delay one of said  $n$  output signals.

31. (Original) The invention of Claim 28 wherein said target simulator further includes second means for applying a Doppler frequency to each of said  $n$  output signals.

32. (Original) The invention of Claim 31 wherein said second means includes  $n$  frequency generators, each frequency generator adapted to apply a Doppler frequency to one of said  $n$  output signals.

33. – 36. (Canceled)

- 2 -

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